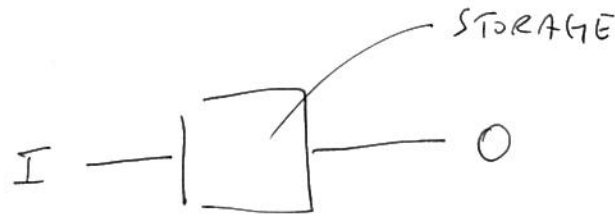
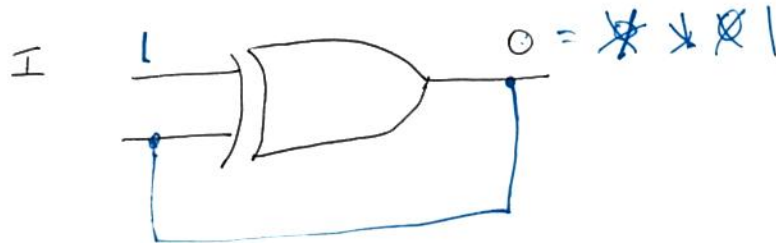


STORING NUMBERS

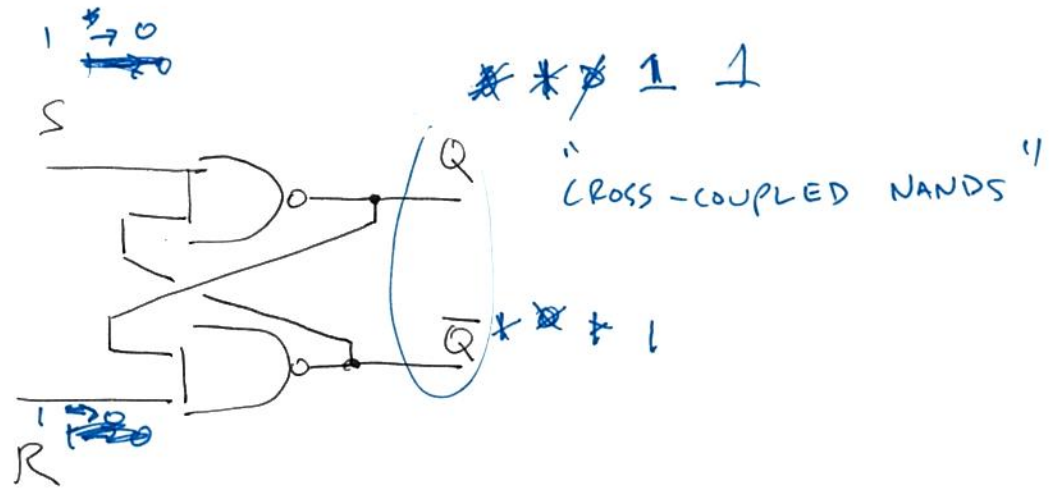


I	WR. 0	NEXT 0
0	0	0
0	1	1
1	0	1
1	1	0

?? ?



S - R LATCH

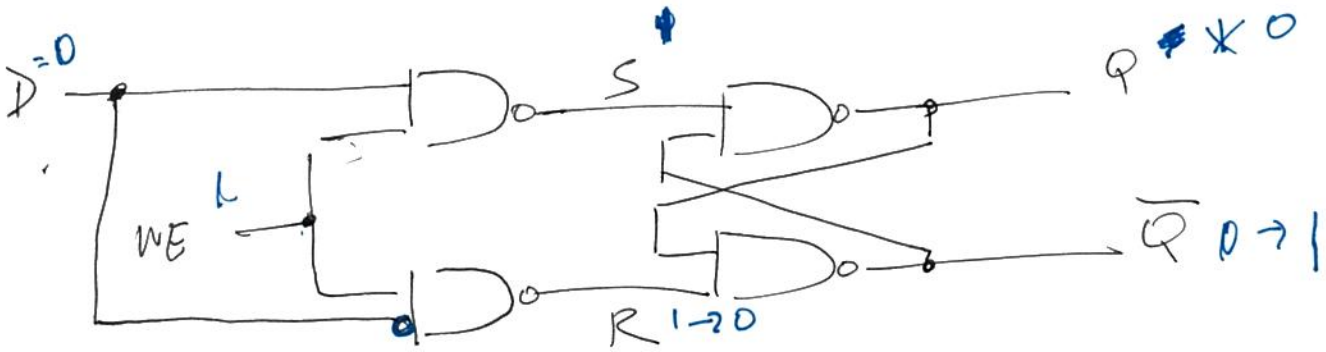


"QUIESCENT"
STATE $\Rightarrow S, R = 1$

CANNOT HAVE $S, R = 0$!
(AT SAME TIME)

INCONVENIENT!

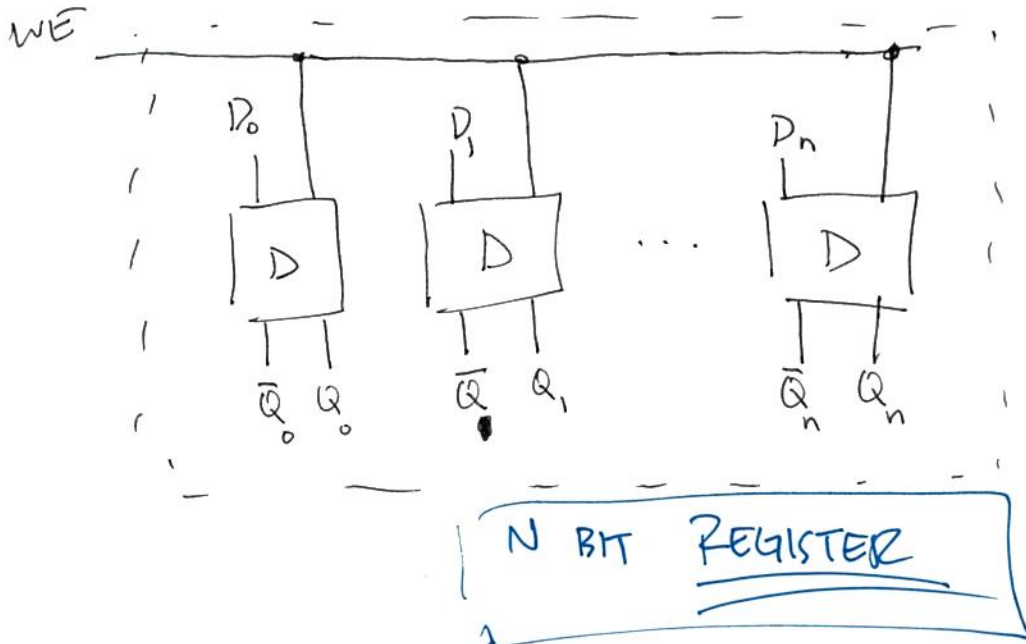
THE "GATED" D LATCH



ALLOWS US TO
"CONTROL" WRITES INTO

STORAGE

($S=R=0$ DOESN'T HAPPEN)



N BIT REGISTER