# Efficient Anytime Anywhere Algorithms for Closeness Centrality in Large and Dynamic Graphs 

Eunice E. Santos, John Korah, Vairavan Murugappan, Suresh Subramanian<br>Department of Computer Science<br>Illinois Institute of Technology, Chicago, USA<br>\{esantos2, jkorah3\}@iit.edu, \{vmuruga1,ssubra20\}@hawk.iit.edu


#### Abstract

Recent advances in social network analysis methodologies for large (millions of nodes and billions of edges) and dynamic (evolving at different rates) networks have focused on leveraging new high performance architectures, paralleldistributed tools and novel data structures. However, there has been less focus on designing scalable and efficient algorithms to handle the challenges of dynamism in large-scale networks. In our previous work, we presented an overarching anytime anywhere framework for designing parallel and distributed social network analysis algorithms that are scalable to large network sizes and can handle dynamism. A key contribution of our work is to leverage the anytime and anywhere properties of graph analysis problems to design algorithms that can efficiently handle network dynamism by reusing partial results, and by reducing re-computations. In this paper, we present an algorithm for closeness centrality analysis that can handle changes in the network in the form of edge deletions. Using both theoretical analysis and experimental evaluations, we examine the performance of our algorithm with different network sizes and dynamism rates.


Keywords-social network analysis; parallel and distributed processing; centrality analysis; dynamic graphs; anytime algorithms

## I. Introduction

Social networks provide the capability to leverage graph algorithms and other computational methodologies to rigorously analyze various social phenomena such as social influence. Depending on what relationships are depicted by the arcs/edges of a social network, the graph can be represented as directed or undirected. A number of social network services such as Twitter, LinkedIn, and a myriad of other similar applications have witnessed explosive growth in the past decade. As a result, these domains generate dynamic network information in the order of millions of nodes and edges. Availability of such large data sets, while significantly extending our understanding of the underlying social phenomenon, has also created new challenges for social network analysis (SNA) research. We now describe the following key issues in performing SNA on large and dynamic social networks.
The most important issue is network size. As the network size grows, computation time and resources required for measuring SNA metrics may increase dramatically[1][2]. Computation time can significantly constrain the utility of

SNA techniques in large-scale networks, especially for time critical applications. A number of recent papers have proposed various methodologies such as parallel and multithreaded tools, novel data structures and efficient graph libraries to perform large-scale graph analysis [3][4][5][6]. While parallelization techniques solve some of the scalability issues, there are other issues such as network dynamism that need to be better addressed. Also, these methodologies do not generally prescribe algorithm designs that take into account tradeoffs between memory availability and performance.

In many SNA tasks such as real-time social media analytics, disaster management, etc., the underlying network is continuously evolving, with nodes and edges being added and/or removed, during analysis. One approach used to address this issue is to restart the analysis of the modified graph from scratch [7]. While this method can be feasible for smaller graphs with occasional network changes, restarting the entire analysis from scratch in larger real-time networks with frequent updates will often yield poor performance. Another method is to analyze a static snapshot of the dynamic network. However, this will only yield approximate results which, depending on the rate of change of the network, can quickly become obsolete. Although there are some graph processing methods[6][8] that are sensitive to networks changes, their main objective is to reduce the load imbalance caused by dynamism. We believe less attention has been given to formulating critical algorithm designs that can truly adapt to different rates of changes in the social networks by efficiently incorporating the new information, and avoiding massive re-computations. Finally, as discussed in the background section, many computational platforms such as shared-memory, distributed-memory, massively multithreaded and other specialized architectures are leveraged to perform large-scale graph analysis. In order to take advantage of various computational platforms, any graph analysis algorithm or framework has to be compatible with these environments.

In order to address these challenges, a parallel/distributed anytime anywhere methodology for large and dynamic social network analysis was proposed by Santos et al. [1], [9]. In the anytime anywhere approach, the large social network graph is decomposed into smaller sub-graphs. Each sub-graph is analyzed and refined over time, while providing nontrivial intermediate results. The quality of these results are
monotonically non-decreasing with respect to the computation time. Moreover, the dynamic changes in the network occurring over the course of analysis are continuously incorporated. In the anytime anywhere methodology, the term anytime refers to the capability of the algorithms to provide nontrivial partial results that will get refined over time. The term anywhere, on the other hand, refers to the capability to incorporate changes to the network whenever it happens and to propagate the effects of these changes to the whole network. We demonstrated our anytime anywhere approach for centrality measurements [1] and maximal clique enumeration problem [2] as part of our previous work. In this work, we provide algorithm designs that can adapt to edge deletions efficiently, and validate this approach for the problem of closeness centrality calculations in social networks. Furthermore, we have provided both theoretical and experimental evaluations of the proposed algorithm. Note that our previous algorithm designs[2] for edge deletions were specific for the maximum clique problem.

## II. Background

A number of graph libraries [4][10], computational frameworks [3][7], tools [5][6], and data structures[3][10] have been proposed for large-scale graph analysis. However, even with the existing computational capabilities researchers and analysts are overwhelmed with the ever-increasing size of the social network datasets. In addition to the enormous scale, these data sets exhibit various degrees of dynamism, which further impedes the network analysis. Some of the prominent single machine, shared-memory based graph analysis tools are LEMON [11], Pajek ${ }^{1}$, igraph [12], NetworkX [13], UCInet [14], etc. Pajek and UCInet are graph analysis, software tools that provide implementations of various graph analysis algorithms, and provisions for graph visualization. LEMON, NetworkX and igraph are graph libraries that provides APIs and other useful graph data structures for performing graph analysis. Due to issues such as limited memory size, these tools can only provide analysis for small to medium scale (hundreds of thousands of nodes) graphs. Additionally, these systems are not designed to handle dynamic graphs.

MultiThreaded Graph Library (MTGL) [10] and GraphCT [3] provide graph analysis algorithms and data structures for massively multithreaded, shared-memory platforms. However, massively multithreaded, shared-memory tools are generally developed for specialized supercomputer architectures, such as Cray Multi-Threaded Architecture (MTAs) and eXtreme MultiThreaded Machine (XMTs). Also, they are not suited for distributed-memory clusters. The primary focus of massively multithreaded graph libraries is to provide high performance graph data structures and algorithms for large-scale graph analysis that are fine tuned
for massive shared-memory and many thread processor architectures. Our anytime anywhere framework, on the other hand, is computational platform independent and can be used to develop dynamic large-scale algorithms for both cluster computing platforms and other specialized architectures.

MapReduce[15] is a popular framework that provides a simple, scalable and fault tolerant programming model for developing parallel applications. It is specifically designed for processing large amounts of data stored on commodity clusters. Large-scale graph analysis tools such as Surfer [5] and PEGASUS [4] were developed based on the MapReduce framework. However, most graph analysis algorithms are iterative in nature [16], and MapReduce systems are not well suited for such tasks. Pregel [7], a vertex-centric and Bulk Synchronous Parallel (BSP) model based graph analysis platform, was designed to address these issues. In this model, the computation is divided into a sequence of iterations or supersteps. During each superstep, a user defined function is performed at each graph vertex. Messages are exchanged between the vertices at the end of each superstep. This iterative framework can be used to formulate a number of graph algorithms. In addition, a user defined function can be invoked for each vertex, theoretically in parallel, so the maximum number of parallel tasks in the system could be as high as the number of vertices in the graph, thereby making this framework highly scalable. This framework provides ease of programming due to the synchronicity imposed by the supersteps. Giraph ${ }^{2}$, Hama ${ }^{3}$, Mizan [6] and Pregelix [17] are some of the popular Pregel based graph analysis systems [18]. These systems provide efficient data structures, APIs, and platforms for large-scale graph analysis. However, MapReduce and Pregel are programming frameworks, and do not specify efficient algorithm designs for handling dynamic graphs.

A large part of existing work on SNA algorithms, especially for computing shortest path related metrics for dynamic graphs [19][20], is focused on providing efficient serial algorithms. While these solutions perform well for the analysis of small to medium sized graphs, they are not scalable and are usually inefficient for large-scale graph analysis. While parallel/distributed graph analysis algorithms can overcome the inherent scalability issue in serial algorithms, designing such algorithms involve performing several challenging tasks such as efficiently partitioning the graph to balance workload, handling communication overhead, etc. An anytime anywhere framework addresses these issues efficiently. We have demonstrated our anytime anywhere approach and framework in previous work by developing and evaluating SNA algorithms for centrality analysis [9] and maximum cliques [2]. Specifically, we demonstrated the capability of our framework to deal with increase and decrease in edge weight during centrality analysis [1]. We have also designed algorithms to deal with

[^0]${ }^{3} \mathrm{http}$ ://hama.apache.org/
addition and deletions of edges during maximal clique enumeration [2]. In this paper, we further extend our capability to analyze dynamic graphs by designing a closeness centrality algorithm that can handle edge deletions. Although we have developed anytime anywhere algorithms to handle edge deletions for maximum clique enumeration, edge deletions generate wider network ripple effects in closeness centrality analysis, thereby requiring different algorithmic design strategies.

In many SNA scenarios it is beneficial to obtain nontrivial intermediate results earlier during the course of analysis, which is one of the capabilities of the anytime anywhere framework [2][1]. In many real-world networks such as online social networks, the underlying graph structure is highly dynamic. In such networks, vertices and edges are continuously evolving and analyzing these graphs in realtime has to be a continuous process. Thus, it is critical for the graph analysis algorithms to be able to accommodate the dynamic graph changes during the course of analysis. Some recent works addressed the issues caused by dynamic largescale graphs from a load balancing perspective [6][8]. Our key focus in this work is to design algorithms that can efficiently handle dynamic structural changes, and refine the results instead of restarting or re-computing the entire analysis.

## III. Anytime Anywhere Methodology

The anytime anywhere framework handles the analysis of large and dynamic real-world social networks by providing two key features: nontrivial intermediate results and handling network dynamism. The central idea in our framework is to divide the large-scale graph into smaller sub-graphs and analyze these sub-graphs by incrementally refining and combining their results. In general, many graph analysis algorithms are iterative in nature and exhibit anytime and anywhere properties [1][21][22]. Anytime algorithms have the following properties: 1) Interruptibility: The algorithm can be paused at any intermediate step, and a valid result can be obtained, 2) Preemptability: The algorithm can be suspended and restarted with minimal overhead, 3) Result Quality: A measure for the quality of the intermediate results generated by the algorithm can be defined, and 4) Predictability: The quality of the results has a monotonically non-decreasing relation with the amount of computational time and resources available to the algorithm. Moreover, performance profiles of an anytime algorithm can be formulated that allow for the prediction of the quality of future results.

On the other hand, algorithms with anywhere property have the ability to incorporate changes occurring in any part of the network, and efficiently propagate its effects across the whole network. Social network analysis performed using our methodology leverages these innate properties in graph algorithms such as the Floyd-Warshall's algorithm for calculating all pairs shortest paths (APSPs). Our anytime anywhere methodology consists of three phases: domain
decomposition (DD), initial approximation (IA) and recombination (RC) and has been discussed in [1][2]. In the DD phase, the large social network graph is partitioned into smaller sub-graphs, which are then distributed among a set of compute nodes such that the workload is evenly distributed. Initial results computed during IA phase is incrementally combined, and refined in the RC Phase. Furthermore, RC phase incorporates any dynamic changes to the network. Detailed explanations for each of these phases are provided below.

## A. Domain decomposition

Many social networks have community structures, such that nodes within a community are more tightly connected (i.e. share more edges), than nodes between different communities. Therefore, partitioning the graphs along these community structures helps to ensure that communication and computation costs are distributed equally across the processors. Computational costs for processing each subgraph depends on the number of vertices in the sub-graph. Whereas, communication cost is determined by cut-edges and cut-size. Cut-edges are edges, whose endpoints belong to different sub-graphs, and cut-size is the number of such edges. Intuitively, sub-graphs with higher cut-size will share more edges with other sub-graphs, thereby increasing the amount of communications performed during each iterative step. On the other hand, sub-graphs with smaller number of cut-edges will provide better anytime results, since most information required by the sub-graph are self-contained.

To address these issues, we used a partitioning methodology based on the Parallel Graph Partitioning and Fill-reducing Matrix Ordering (ParMETIS) algorithms [23]. ParMETIS partitions the graph such that the resulting subgraphs have minimal cut-edges. Naturally, as the number of cut-edges becomes smaller the amount of communications performed in the RC phase reduces significantly.

## B. Initial approximation

The IA phase provides the preliminary approximation results for the whole network. The quality of results obtained in this phase is dependent on the computation time applied and the amount of information contained in the sub-graphs. Algorithms employed in the IA phase are designed such that the partial results calculated from each sub-graph can be combined and refined further in the RC phase.

## C. Recombination

In the RC phase, the partial results are incrementally combined and refined to obtain a final result. It is iterative, with each iteration consisting of two key steps: 1) Refine the results obtained from the previous RC step, and 2) Adopt the dynamic changes in the graph. During each iteration, partial results in each sub-graph is refined based on the updates obtained from the neighboring sub-graphs. Furthermore, as discussed in earlier sections, the vertices and edges of dynamic social networks change over time. The RC phase continuously incorporates these changes into the network and
propagates the effects to the whole network. Communication costs are a major component of the overall run time of parallel/distributed algorithms. Therefore, a careful design of communication schedules based on the analysis requirements and on the computational platform is necessary. The anytime anywhere framework supports rigorous performance analysis using performance models such as $\operatorname{LogP}$ [24], which in turn, provides insights for choosing the appropriate communication schedule. Note that the RC phase does not impose a fixed communication model or schedule. Moreover, the framework is also platform independent.

## IV. ANYTIME ANYWHERE ALGORITHM DESIGN FOR CLOSENESS CENTRALITY

Closeness centrality[25] is a key social network analysis (SNA) metric for identifying important actors/nodes. Such nodes figure prominently in the social process being analyzed as they play an important role in spreading social interactions and information. Closeness centrality analysis requires the computation of all pairs shortest paths (APSPs) in the network. Due to the computational challenges of calculating APSPs of large and dynamic networks, closeness centrality is a suitable problem to demonstrate the strengths of the anytime anywhere framework. Various heuristic based algorithm designs have been proposed for finding approximate centrality values [26], [27]. However, these designs require a "restart" when the graph changes and do not efficiently reuse already calculated partial solutions. Our anytime anywhere framework [1] was formulated to deal with large and dynamic social networks and we will provide algorithm designs based on this framework that efficiently deal with graph changes.

## A. Closeness centrality

Definition 1: Given a graph $G(V, E)$ with a set of vertices $V$ and a set of edges $E$ such that $|V|=n$ and $|E|=m$, closeness centrality $C_{c}$ of a vertex $u \in V$ is defined as,

$$
C_{c}(u)=\frac{1}{\sum_{k=0}^{n} d\left(u, v_{k}\right)}
$$

where $d(u, v)$ represents the shortest path distance between the vertices $u$ and $v$.

In [1], we presented algorithms for handling dynamic increases and decreases in edge weight. We now extend the work to efficient algorithm design for dynamic edge deletions. Unlike changes to edge weight, edge deletions cause substantial changes to the structure of the network. A deleted edge can completely disconnect a sub-graph from the rest of the network. Our focus is on algorithm designs for efficiently reusing the partial solutions and reduce recomputations during edge deletions. The other challenge for parallel/distributed algorithm design is that, dynamic changes can cause load imbalances. However, the question of dynamic load balancing will be dealt in future work.

Below, we provide details on the algorithm design for closeness centrality, followed by discussions on the designs for handling dynamic changes. As we described earlier,
algorithms designed using our anytime anywhere approach have three phases: 1) Domain decomposition (DD), 2) Initial approximation (IA), and 3) Recombination (RC).

## B. Domain decomposition

We used a partitioning algorithm based on the Parallel Graph Partitioning and Fill-reducing Matrix Ordering (ParMETIS) algorithm [23]. The partitioning algorithm provides roughly $\frac{n}{p}$ vertices in each partition [28], where $n$ and $p$ are the number of vertices and number of processors respectively. Following the methodology in ParMETIS algorithm, the graph partitioning in DD consists of the following steps: coarsening, folding, initial partitioning, uncoarsening and un-folding. One of the limitations of ParMETIS is that it requires perfect square number of processors. Since the anytime anywhere framework does not impose such restrictions on processor count, we overcome this limitation in the graph partitioning by idling a subset of the processors during the DD phase.

Coarsening is the first phase of ParMETIS algorithm. During coarsening, multiple vertices are merged together into super nodes. Heavy edge matching, random matching, light edge matching, etc. are some of the strategies used for merging vertices. For balanced partitioning in our implementation, we ensure that during initial partitioning, no partition gets more than $O\left(\frac{n}{p}\right)$ vertices, where $n$ is the number of vertices in the graph. In the ideal case, the graph is coarsened to $p$ partitions, where $p$ is the number of processors. However, achieving this level of coarsening in small world/scale free networks is challenging and time consuming. Therefore, we set a threshold of $20 \%$ as the required reduction in number of vertices. Since initial partitioning utilizes a randomized serial bisection algorithm, it was performed using all processors, and the best partition based on the edge cut-size is selected.

## C. Initial approximation

In the initial approximation phase, the all pairs shortest paths are calculated for the sub-graphs assigned to each processor. Any all pairs shortest paths algorithm such as Dijkstra's or Floyd-Warshall's algorithm, that exhibits the anytime and anywhere properties can be used here. Since shortest paths for different sources can be computed in parallel, we used a multithreaded version of Dijkstra's single source shortest path algorithm in our implementation. We used a binary search tree for the graph traversal aspect of the algorithm.

## D. Recombination

Recombination algorithm for closeness centrality uses the partial results calculated during initial approximation phase and further refines them on every iteration. In the recombination phase, updates from multiple sub-graphs are combined in an iterative process to generate better result. We used Distance Vector Routing (DVR) algorithm [29], a wellknown distributed algorithm, to process incremental updates.

In DVR, each processor notifies its neighboring processors periodically (in this case during every RC step) about changes in its sub-graph, by sending its updated distance vectors. Distance vector (DV) of node $v$ stores the currently known shortest path distances from node $v$ to all other nodes in graph $G$.

During each iteration, partial results from each sub-graph are propagated to its neighboring processors through boundary vertices. Recombination phase makes use of the fact that a boundary vertex acts as a gateway to its corresponding sub-graph. Therefore, there is a need to only communicate the updates for the boundary vertices. This significantly reduces the amount of data communicated during each iteration.

| 1. INPUT: $P=\left\{P_{1}, \ldots P_{i}, \ldots P_{p}\right\} / /$ set of processors assigned to the problem |  |
| :---: | :---: |
|  | INPUT: $n \quad / /$ number of vertices in the overall graph $G$ |
|  | INPUT: $G_{i}\left(V_{i}, E_{i}\right) \quad / /$ sub-graph assigned to processor $P_{i}$ |
|  | INPUT: $D V_{i}^{0} \quad / / D i s t a n c e ~ V e c t o r s ~\left(\left\|V_{i}\right\| X n\right)$ for subgraph $G_{i}$ generated in IA phase |
|  | FOR EACH processor $P_{i}$ do in parallel |
| 6. | $k=0 \quad / /$ initialize the recombination step index |
| 7. | DO //propagate updates to neighboring processors |
| 8. | $k=k+1 / /$ increment the recombination step index |
| 9. | $\mathbf{F O R} j=1$ to $p$ |
| 10. | IF $i \neq j$ |
| 11. | RECV DVs of external boundary nodes from processor $P_{j}$ in messages of size $\alpha$. |
| 12. | Update local boundary vertices using the DVs of external boundary vertices. |
| 13. | ELSE |
| 14. | SEND DVs of respective external boundary vertices to $(p-1)$ processors in messages of size $\alpha$. |
| 15. | END FOR |
| 16. | Perform dynamic changes |
| 17. | Update the local DVs to generate $D V_{i}^{k}$ |
| 18. | UNTIL $k=p-1$ OR no more updates in any processor |
|  | END FOR |

Figure 1. Pseudo-code 1: Recombination algorithm for closeness centrality

In Figure 1, lines $9-15$ represent the personalized all-toall communication that happens during the recombination phase. We use a communication schedule that avoids network flooding by ensuring that only one message traverses the network at any given time. In this schedule, only one processor sends at any one time, and each sender sends its updates to neighboring processors sequentially before the next sender starts communicating. Other forms of all-to-all communication scheduling (such as, linear wrap around, pair-
wise exchange, etc.[30]) exist in which multiple processors communicate simultaneously. However, in practice, these schedules can lead to network congestion, and result in unpredictable performance. This is especially true for the Ethernet network[30] fabric, which was used in our experimental setup, described in later sections. Even though our all-to-all scheduling takes $O\left(p^{2}\right)$ steps (where $p$ is the number of processors) in the worst case, it mitigates excessive network flooding. In each communication step, the boundary vertices of every sub-graph receive the updated values, which are then used to update the shortest paths by utilizing the Floyd-Warshall's algorithm.

Dynamic changes to the network can be incorporated at any step during the recombination phase. However, in order to increase the accuracy of the data that is being propagated to other processors; dynamic changes (Figure 1 line 16) to the graph are applied before local computations are initiated. This ensures that the data being shared between processors is up to date with the changes in the local sub-graph. In a static graph, maximum number of iterations/steps in the recombination ( RC ) phase is bounded by the number of subgraphs, which in this case is equivalent to the number of processors, $p$. On the other hand, updates can occur during any iteration in a dynamically changing graph. Therefore, refinement of the results in the RC phase continues until there are no more updates to be shared between processors.

## E. Anytime anywhere approach for edge deletion

INPUT: $P=\left\{P_{1}, \ldots P_{i}, \ldots P_{p}\right\} \quad / /$ set of processors assigned to the problem
INPUT: $n \quad / /$ number of vertices in the overall graph $G$
INPUT: $G_{i}\left(V_{i}, E_{i}\right) \quad / /$ sub-graph assigned to processor $P_{i}$
INPUT: $A D J_{i} / / A d j a c e n c y$ list of the vertices $V_{i}$ of the sub-
graph $G_{i}$; the list also contains the boundary nodes in all other
sub-graphs which are connected to a vertex in $G_{i}$
INPUT: $D V_{i} \quad / / D$ Distance Vectors $\left(\left|V_{i}\right| X n\right)$ for sub-graph $G_{i}$
INPUT: $E=\left\{\left(a_{1}, b_{1}\right), \ldots\left(a_{j}, b_{j}\right), \ldots\left(a_{l}, b_{l}\right)\right\} \quad / /$ set of edges to be deleted
INPUT: $W^{\prime}=\left\{w_{1}^{\prime}, \ldots, w_{j}^{\prime}, \ldots, w_{l}^{\prime}\right\} / /$ weights of the edges to be deleted
8. INPUT: $P^{\prime}=\left\{P_{1}^{\prime}, \ldots, P_{j}^{\prime}, \ldots, P_{l}^{\prime}\right\} / / p r o c e s s o r ~ c o n t a i n i n g ~ t h e ~$ vertex $a_{j}$ of the edge to be deleted
9. INPUT: $P^{\prime \prime}=\left\{P_{1}^{\prime \prime}, \ldots, P_{j}^{\prime \prime}, \ldots, P_{l}^{\prime \prime}\right\} / / p r o c e s s o r$ containing the vertex $b_{j}$ of the edge to be deleted
10. FOR EACH processor $P_{i}$ do in parallel
11. FOR EACH edge $\left(a_{j}, b_{j}\right)$ to be deleted
12. IF $b_{j}$ is a node in sub-graph $G_{i}$
13. SEND row $D V_{i}\left[b_{j}\right]$ to all other processors //using tree broadcast
ELSE RECV row $D V_{i}\left[b_{j}\right]$ from processor $P_{j}^{\prime \prime}$
END IF
IF $w_{j}^{\prime}==D V_{i}\left[a_{j}\right]\left[b_{j}\right]$ FOR EACH $u \in V_{i}$ FOR EACH $v \in V$

IF $D V_{i}[u][v]!=\infty$ AND $D V_{i}[u][v]==D V_{i}[u][a]+w_{j}^{\prime}+D V_{i}[b][v]$

| 21. | $D V_{i}[u][v]=\infty$ |
| :---: | :---: |
| 22. | Enqueue edge $(u, v)$ into Queue Q |
| 23. | END IF |
| 24. | END FOR |
| 25. | END FOR |
| 26. | END IF |
| 27. | IF $a_{j} \in V_{i} / /$ update the adjacency list after deleting edge |
| 28. | DELETE edge $\left(a_{j}, b_{j}\right)$ from $A D J_{i}\left[a_{j}\right]$ and $A D J_{i}\left[b_{j}\right]$ |
| 29. | IF $b_{j} \notin V_{i}$ and $A D J_{i}\left[b_{j}\right]$ is empty $/ /$ for cut-edge |
| deletion |  |
| 30. | Notify $P_{j}^{\prime \prime}$ to stop sending DV of $b_{j}$ to $P_{j}^{\prime}$ |
| 31. | END IF |
| 32. | END IF |
| 33. | END FOR |
| 34. | WHILE Q is not empty |
| 35. | Dequeue $(u, v)$ from Q |
| 36. | FOR EACH neighbor $u^{\prime}$ of $u$ in sub-graph $G_{i}$ |
| 37. | IF $D V_{i}[u][v]>D V_{i}[u]\left[u^{\prime}\right]+D V_{i}\left[u^{\prime}\right][v]$ |
| 38. | $D V_{i}[u][v]=D V_{i}[u]\left[u^{\prime}\right]+D V_{i}\left[u^{\prime}\right][v]$ |
| 39. | mark $D V_{i}[u][v]$ as updated $/ / f o r$ focal computations |
| and propagation through $R C$ |  |
| 40. | END IF |
| 41. | END FOR |
| 42. | END WHILE |
| 43. | END FOR |

Figure 2. Pseudo-code 2: Anytime anywhere approach for edge deletion
In a social network, continuing an ongoing analysis after an edge deletion is a challenging task. However, as described in earlier sections, algorithms such as all pairs shortest paths have anytime characteristics, where removal of an edge may not alter every shortest path. Therefore, the shortest paths that are not affected by the deleted edge need not be re-computed. Moreover, the shortest paths that are altered can be efficiently recomputed using the sub-paths information, which were previously generated and stored. This is the core idea behind our algorithm for efficiently dealing with edge deletions in dynamic social networks.

The delete edge algorithm, described in this paper, makes use of the fact that the shortest path values affected by the deletion of the edge $e(a, b)$ are those, which contain the vertices $a$ and $b$ in their paths. When an edge is deleted, each processor $P_{i}$ checks the distance vectors (DVs) of its local and boundary vertices to see if any of the paths went through that edge. This can be done by checking if, $D V_{i}[u][v]==$ $D V_{i}[u][a]+w(a, b)+D V_{i}[b][v]$ where $u \in V_{i}$ and $v \in V$.

In order to perform this check, the processor that owns vertex $b$ broadcasts the distance vector of $b\left(D V_{i}[b]\right)$ to all other processors (Figure 2 line 11 -13). Every path that satisfies this equality condition is reset to infinity. Once the paths, that contain the deleted edge, are reset, they have to be recalculated. To do this efficiently, we use the fact that if there exists another path between vertices $u, v$, then it has to go through a neighboring vertex ( $u^{\prime}$ ) of $u$. Hence we only need to check the paths going through the neighboring vertices of $u$. Note that this could be an overestimation and
there may be a better path between vertices $u$ and $v$. A shorter path will then be discovered during successive recombination steps. After the deletion of an edge, if there is no path between $u$ and $v$ that goes through $u^{\prime}$ it is either because there exists no path between $u$ and $v$ or the new path has not been discovered yet. The latter case will also be handled during successive iterations of the recombination phase. When a new shortest path is discovered, it is communicated to the neighboring processors, and their boundary vertices and local DVs are updated.

## V. Algorithm Analysis (COMPLEXITY/RUN time ANALYSIS)

In this section, we analyze the delete edge algorithm described in the previous section, along with the time complexity of the initial approximation (IA) and recombination (RC) phases. We do not include the analysis of the domain decomposition (DD) phase as graph partition is only done once, and dynamism in the graph does not trigger repartitions in our implementation. Given a graph $G(V, E)$ with a set of vertices $V$ and a set of edges $E$ such that $|V|=n$ and $|E|=m$. Let $P=\left\{P_{1}, P_{2}, \ldots, P_{p}\right\}$ be the set of compute nodes or processors available for processing the graph. A processor $P_{i}$ is assigned a vertex set $V_{i} \subseteq V$. During the DD phase, the vertex set $V$ is partitioned into subsets of vertices $\left\{V_{i}\right\}_{i=1}^{p}$ such that $\bigcup_{i=1}^{p} V_{i}=V$ and $\bigcap_{i=1}^{p} V_{i}=\phi$. $G_{i}\left(V_{i}, E_{i}\right)$ denotes the sub-graph of $G$ induced from the vertex set $V_{i}$ and assigned to processor $P_{i}$ during the DD phase. Given an edge $e(u, v) \in E$, where $u \in V_{i}$ and $v \in V_{j}$, if $V_{i} \neq$ $V_{j}$ then $e(u, v)$ is referred to as a cut-edge, and $u$ and $v$ are referred to as a boundary node of $V_{i}$ and $V_{j}$, respectively. Also $C_{i} \subseteq V_{i}$ denotes the set of boundary nodes in $V_{i}$. Moreover, given a vertex $v \in C_{i}$, cut_deg $(v)$ is defined to be the number of cut-edges, where $v$ is one of the vertices in the edges. Furthermore, we define $\gamma_{i}=\max _{v \in C_{i}} c u t \_\operatorname{deg}(v)$.

We leveraged the LogP distributed memory model[24] to analyze the asymptotic run time (provided below) of the various phases of our anytime anywhere algorithm. The LogP model utilizes the following four parameters: 1) Latency (L): the delay incurred by the message to reach the target processor, 2) Overlap (o): the amount of time spent exclusively by the source processor on sending a message or the time spent exclusively by the target processor on receiving a message, 3) Gap (g): the minimum time interval between two consecutive send operations at the source processor or two consecutive receive operations at the target processor, and 4) Processors (p): the number of processors utilized.

## A. Initial Approximation

During the initial approximation (IA) phase, each processor creates partial results from the information available within its local sub-graph. These partial values can be combined later with the information from other sub-graphs
during recombination. Therefore, it is essential to use or design algorithms for IA such that they exhibit the anytime and anywhere properties. We used Dijkstra's algorithm to calculate all pairs shortest paths within the local sub-graph during initial approximation. The runtime for Dijkstra's algorithm[31] on processor $P_{i}$ using a binary search tree is $O\left(\left|V_{i}\right|\left|E_{i}\right| \log \left|V_{i}\right|\right)$. Assuming that in the worst case, the processors are assigned fully connected sub-graphs, the runtime of IA is $O\left(\frac{n^{3}}{p^{3}} \log \frac{n}{p}\right)$. However, real world social networks tend to exhibit the scale free property with relatively few high degree nodes. Therefore the sub-graphs generated during domain decomposition are generally not fully connected graphs. If support for multithreads is available on the processors, the shortest paths for different sources in the Dijkstra's algorithm can be calculated in parallel. The run time of IA with multithread support is $O\left(\frac{n^{3}}{\tau p^{3}} \log \frac{n}{p}\right)$ where $\tau$ is the number of threads used in each processor.

## B. Recombination

The worst case analysis of the recombination phase is provided here. During the recombination (RC) phase (refer pseudo-code in Figure 1), the size of data that is communicated by a processor depends on the number of boundary vertices of the sub-graph assigned to it. In the worst case, processor $P_{i}$ has to send $n\left|C_{i}\right|$ data items to all the other processors. Note that the data is communicated in messages of size $\alpha$ in order to mitigate network flooding and to also constrain memory requirements. Time taken for updating the distance vector (DV) of boundary vertices (see Figure 1, line 12 ) in processor $P_{i}$ is $O\left(n \gamma_{i}\left|C_{i}\right|\right)$. In real world social networks with the scale free property, we make the following assumption on the maximum number of cut-edges that a boundary vertex assigned to processor $P_{i}$ [32], [33][34] can have: $\gamma_{i} \leq \frac{n}{\log n}$. The time taken for updating the DV of the sub-graphs using the updated information from boundary vertices (see Figure 1, line 17) is $O\left(\frac{n^{3}}{p}\right)$.
The run time to complete the RC phase is:

$$
O\left(\frac{n^{3}}{p}+\frac{n^{3}}{\log n}+\frac{n^{2}}{\alpha} p^{2} L+n^{2} p^{2} g\right)
$$

## C. Edge Deletion using anywhere approach

In this section we analyze the performance of the delete edge anytime anywhere approach (refer pseudo-code in Figure 2). Let $l$ be the number of edges to be deleted.

- Figure 2 Line $11-16$ : This communication utilizes a tree broadcast approach. Hence the time complexity for this operation is $O(l \log p(L+$ $n g)$ ).
- Figure 2 Line 17 - 26: The time complexity for equality comparisons by a processor $P_{i}$, to check if the edge is present in any of the paths, is $O\left(l\left|V_{i}\right| n\right)$.
- Figure 2 Line 34-42: In the worst case all the paths in $G_{i}$ are affected, hence the time taken to recalculate new paths using its neighbors is $O\left(n\left|V_{i}\right|^{2}\right)$.
Total runtime of anytime anywhere delete edge algorithm in the worst case is $O\left(\frac{n^{3}}{P^{2}}+l \frac{n^{2}}{P}+l \log p(L+n g)\right)$.
It may be noted that the analysis provided above takes into account the additional computations required to incorporate changes in the DVs of the sub-graphs so that the processors can continue with the RC phase. An additional $(p-1)$ steps may be needed once an edge is deleted regardless of how many RC steps have already been performed.


## VI. IMPLEMENTATION AND EXPERIMENTAL RESULTS

We implemented and tested our approach on a distributedmemory cluster with 32 compute nodes connected over 1 $\mathrm{Gb} / \mathrm{s}$ Ethernet network. Each compute node had dual Intel Xeon E5 ( 1.8 GHz ) processors (total of 16 cores per node) and 32 GB of memory. The algorithms were implemented in C++. MPICH's Message Passing Interface (MPI) library was used for inter-node communications, and the multithreaded aspect of the IA phase was implemented using OpenMP.

## A. Experimental setup

As real world social network graphs tend to exhibit the scale free property, we generated a test bed of medium sized social networks using the Pajek tool, with the node degree of the networks obeying the power law property. Keeping in mind the prevalence of various online social networking applications such as Twitter and YouTube, where the underlying social networks contain directed edges, our experimental study focused on the closeness centrality analysis of directed graphs. However our anytime anywhere approach is applicable to both directed and undirected graphs, and our current implementation can also handle undirected graphs. As this is preliminary work, the experimental results presented in this paper, focus on understanding the performance of our approach for handling deletions of regular edges in medium sized graphs. In future work, we plan to provide a more comprehensive experimental study that includes performance results with larger networks, and with deletions of both regular edges and cut-edges.

Although, there are existing work on analysis of dynamic networks, their focus has been on designing efficient data structures to handle graph changes or studying the effects of load balancing. To the best of our knowledge the anytime anywhere framework described in this paper, is the only work that focuses on the parallel algorithm design aspects of the dynamic social network problems. Due to this reason, we compared our anytime anywhere approach with the baseline restart approach, where the network analysis has to be restarted from scratch when an edge is deleted. The baseline algorithm also has the domain decomposition (DD), initial approximation (IA), and recombination (RC) phases. The crucial difference is that the baseline algorithm has to restart
the analysis, i.e., redo both the IA and RC stages when the network changes.

## B. Results

In this section we report the performance results of our edge deletion algorithm. According to the performance analysis in Section V, the stage in the analysis at which the change (i.e. one or more edge deletions) happens, size of change and the rate at which the change happens has significant impact on the performance of our anytime anywhere algorithm. Based on these factors, we expect the baseline approach to perform better when edge deletions happen at an initial stage of the analysis. However, when the change occurs at a later stage, the anytime anywhere approach should benefit from the precomputed partial results. In addition, the size of the change affects the number of shortest paths that have to be recomputed. In order to understand the performance variations caused by these factors, we designed two experiments. In the first experiment, we keep the rate of change low by selecting a specific recombination step and introducing edge deletions during that step. In the second experiment, we introduce edge deletions at multiple points during the graph processing. Therefore the dynamism rate is much higher in the second experiment.

Figure 3 shows the results for the first experiment which was performed on a graph with 50,000 vertices using 16 processors. In separate experimental runs, edge deletions were introduced during the initial ( RC 0 ) and later ( RC 09 ) steps of the RC phase. Note that the edges deleted were equally divided among the processors. As mentioned in Section V.C, our anytime anywhere methodology for edge deletion has a worst case running time of $O\left(\frac{n^{3}}{p^{2}}+l \frac{n^{2}}{p}+\right.$ $l(L+n g) \log p)$. The dominant terms in the run time are $\left(l \frac{n^{2}}{p}\right)$ and $\left(\frac{n^{3}}{p^{2}}\right)$, which correspond to the time complexity for determining the affected shortest paths and recalculating those paths, respectively. When edge deletions occur during the initial steps of recombination (e.g. RC0), the number of informative partial results available is low. Hence the time taken to calculate the shortest paths by the baseline and the anytime anywhere method is similar. However, the anywhere approach has an additional $\operatorname{cost}\left(l \frac{n^{2}}{p}\right)$ of determining the paths that are affected by the deleted edges, which is dependent on the number of deletions, $l$ occurring at a particular RC step. As a result, for smaller number of edge deletions both methods perform similar whereas, as the number of deletions increases, the anytime anywhere method takes longer due to the additional computational and/or communication overhead.


Figure 3. Performance comparison of anytime anywhere vs. baseline edge deletions at recombination (RC) step RC0 and RC9 using 16 processors on graph with 50,000 vertices


Figure 4. Performance comparison of anytime anywhere vs. baseline incremental edge deletions using 8 processors on graphs with 50,000 and 100,000 vertices

For changes happening during later stages of the RC phase, the time for re-computation is less when compared to the baseline due to the utilization of partial results. Therefore, our anytime anywhere method performs better than the baseline for smaller number of edge deletions. However, as the number of changes increases, the time taken to identify and recalculate the affected shortest paths also increases, thereby affecting the performance of our anytime anywhere approach. Note that this experiment requires the baseline to restart only once as all the edge deletions occur in the same RC step. The performance of our approach should be significantly better when edge deletions occur in multiple steps of the RC, as the baseline now has to be restart multiple times.

Our next set of experiments were designed to understand the effect of multiple changes during the course of analysis. In these scenarios, we expect the anytime anywhere approach
to perform better since baseline method does not utilize partial results, and has to restart from scratch for every change. To study the performance of our anytime anywhere approach under these conditions, we introduce edge deletions across multiple iterations. Given $\lambda$, the total number of edge deletions, that are introduced in the network during $k$ RC steps or iterations, i.e. $l=\frac{\lambda}{k}$. For the baseline, this would require restarting the analysis $k$ times, each time with the modified network. This would require a significant amount of time and resources. To simplify the experimental analysis, we ran the baseline after deleting the specified number of edges $(\lambda)$ from the network and measured the runtime for each iteration separately. This ensures a computational advantage for the baseline algorithm as it essentially processes a smaller network (in terms of number of edges) than the anytime anywhere approach. This also helps in reducing the experimentation time, since essentially we need to run the baseline only once.


Figure 5. Performance comparison of anytime anywhere vs. baseline incremental edge deletions using 16 processors on graphs with 50,000 and 100,000 vertices

Figure 4 and Figure 5 shows the incremental edge deletions on 8 and 16 processors respectively. In the experimental run with 8 processors, the $\lambda$ edge deletion operations are split equally across 8 iterations. On the other hand, in the run with 16 processors, edge deletions are split across 10 iterations. Here, the baseline method has to restart from scratch every time a change occurs, whereas anytime anywhere method utilizes the partial results without restarting the computations. When compared to the previous evaluations discussed in Figure 3, the number of edge deletions are distributed over the RC phase, which reduces the number of shortest paths that needs to be re-computed at any given iteration. A large number of partial results calculated thus far would still be useful; therefore, our anytime anywhere approach performs much better than the baseline in this case. Figure 4 shows the results for graphs with 50,000 and 100,000 vertices computed on 8 processors and Figure 5 shows the results for the same on 16 processors.

The experimental results agree with the theoretical analysis, and it is clear that the anytime anywhere approach performs much better at higher rates of network dynamism. Also, changes occurring, in the final steps of the RC refinement process have a more negative impact on the baseline performance. The results also show that when baseline does not have to restart multiple times, its performance is significantly better when $l$ is large. It is clear from the results that our approach of using anytime anywhere design is highly effective in real world scenarios where the rate of change of networks is high.

## VII. Conclusion

In this work, we designed an anytime anywhere methodology for calculating closeness centrality in large and dynamic social networks that allows for edge deletions. Using both theoretical analysis and experimental evaluations, we demonstrated the ability of our methodology to reuse partial results and to reduce the overheads caused by recomputations. We evaluated our approach to handle edge deletions on a test bed of scale free graphs under different dynamism rates. Performance of the edge delete algorithm was compared with a baseline method. While smaller or less dynamic networks did not readily benefit from our methodology, our method performed significantly better than the baseline under high dynamism rates and as network sizes increased. Our approach is platform independent, and is only constrained by the memory available on the underlying architectures. Overall, these results have further demonstrated the ability of our anytime anywhere framework to handle network dynamisms in social network analysis.

In the future, we plan to extend our anytime anywhere methodology to handle various other network dynamisms such as node additions and deletions. Approaches used in handling social network dynamisms can also be utilized to design algorithms that adapt to hardware failures. Furthermore, we are interested in applying our methodology to analyze and handle load imbalances, caused by node and edge deletions/additions in platforms such as cloud computing.

## Acknowledgement

This work has been supported by NPSG-N00244-15-10046.

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